

Master DUT Test Board and Interface

Number of students: 1 to 3

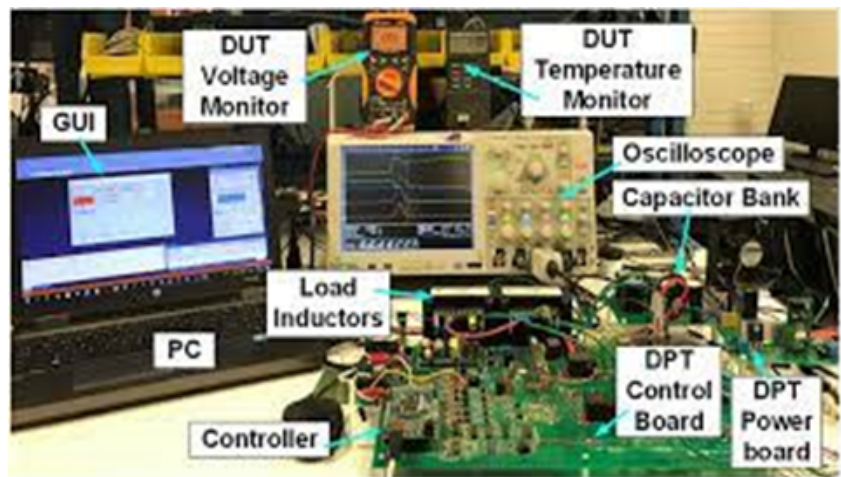
Estimated period of project: 12 months (Senior Project)

Description:

The goal for this project is to develop, design, fabricate and test a PCB and GUI interface for controlling and monitoring future PCBs dealing in High-Speed Signals and Systems (RF, Analog & Digital). During this project, students will be trained in PCB design and layout.

Designing the test-board will be done with CAD software and physical prototyping. Test-boards such as this project are widely used in industry for Research & Development. They are often used for active diagnosis of research chips with high variability in target objectives. Creating a test-board capable of supporting a standardized set of parameters such as Supply Voltage, Reference Current and Control Signals.

Students involved in this project, will learn fundamental design of high-speed circuits and systems, advanced techniques to design, model and analyze such systems, and learn PCB and IC layout techniques. Such a project will certainly be a very valuable experience for students for their future career. Interested students can move



forward and participate in design of such systems and work on cutting edge projects (12 months).

Background: Students with a good background on circuit design are encouraged to apply. Also, students need to be self-motivated and active. Having good knowledge on relevant tools (e.g., Altium, SPICE, Matlab, Python) is a plus.

Contact person: Prof. Armin Tajalli (armin.tajalli@utah.edu) [icas.ece.utah.edu]

** Figure are taken from: <https://ieeexplore.ieee.org/stamp/stamp.jsp?arnumber=9206041>